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01/30/02

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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10062999	01/30/2002	324		2858	

\*\*APPLICANTS: Eldridge Benjamin; Miller Charles;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 10/003,596 10/30/2001

WHICH IS A DIV OF 09/484,600 01/18/2000 PAT 6,339,338

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☐ yes ☒ no

35 USC 119 conditions met

☐ yes ☒ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

P102C1-US

TITLE : Apparatus for reducing power supply noise in an integrated circuit

U.S. DEPT. OF COMMERCE/PAT. & TRADEMARK OFFICE (Rev. 12-93)

7/10/03 Formal Drawings (11 sheets) set 1 1/30/02

NOTICE OF ALLOWANCE MAILED		VINCENT G. NGUYEN		CLAIMS ALLOWED	
2/24/03		Assistant Examiner		Total Claims 31	Print Claim for O.G. 1
ISSUE FEE		N. Le		DRAWING	
Amount Due	Date Paid	Supervisory Patent Examiner		Sheets Drwg.	Figs. Drwg.
1600.00		Technology Center Examiner		19	21
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Print Fig.	18
		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		Application Examiner	

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